


First Named Inventor	Leonard Forbes	SECOND SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT FORM PTO-1449 
Serial No.	10/785,310	
Filing Date	February 24, 2004	
Group Art Unit	2818	
Examiner Name	Tu Tu V. Ho	
Allowed	April 28, 2003	
Confirmation No.	9673	
Attorney Docket No.	400.264US01	
Title: 4F2 EEPROM NROM MEMORY ARRAYS WITH VERTICAL DEVICES		Sheet 1 of 2

U.S. Patent References				
Examiner Initials	Document No.	Issue/Publication Date	Name	Filing Date
TH	2001/0022375 A1	09/20/2001	Hsieh	02/06/2001
	2002/0149081 A1	10/17/2002	Goda	01/30/2002
	2003/0042512 A1	03/06/2003	Gonzalez	08/30/2001
	2003/0043637 A1	03/06/2003	Forbes	08/30/2001
	2003/0113969 A1	06/19/2003	Cho	10/22/2002
	2003/0130356 A1	09/19/2002	Sung	05/16/2002
	2004/0016953 A1	01/29/2004	Lindsay	07/26/2002
	2004/0041203 A1	03/04/2004	Kim	09/02/2003
	2004/0063283 A1	04/01/2004	Guterman	09/29/2003
	4,558,344	12/10/1985	Perlegos	01/29/1982
	4,630,085	12/16/1986	Koyama	02/27/1985
	4,774,556	09/27/1988	Fujii	07/21/1986
	4,785,199	11/15/1988	Kolodny	09/22/1986
	5,461,249	10/24/1995	Ozawa	07/26/1994
	5,463,579	10/31/1995	Shimoji	03/09/1995
	5,620,913	04/15/1997	Lee	05/28/1996
	5,888,868	03/30/1999	Yamazaki	01/22/1997
	5,909,618	06/01/1999	Forbes	07/08/1997
	5,973,352	10/26/1999	Noble	08/20/1997
	5,973,356	10/26/1999	Noble	07/08/1997
	6,091,102	07/18/2000	Sekariapuram	03/13/1998
	6,104,061	08/15/2000	Forbes	02/27/1998
	6,143,636	11/07/2000	Forbes	08/20/1998
	6,191,470 B1	02/20/2001	Forbes	07/08/1997
	6,208,164 B1	03/27/2001	Noble	08/04/1998
	6,222,769 B1	04/24/2001	Maruyama	02/14/2000
	6,238,976 B1	05/29/2001	Noble	02/27/1998
	6,249,460 B1	06/19/2001	Forbes	02/28/2000
	6,337,808 B1	01/08/2002	Forbes	08/30/1999
	6,377,070 B1	04/23/2002	Forbes	02/09/2001
	6,380,585 B1	04/30/2002	Odanaka	06/06/2000
	6,383,871 B1	05/07/2002	Noble	08/31/1999
	6,384,448 B1	05/07/2002	Forbes	02/28/2000
	6,417,049 B1	07/09/2002	Sung	02/01/2000
	6,424,001 B1	07/23/2002	Forbes	02/09/2001
	6,436,764 B1	08/20/2002	Hsieh	06/08/2000
	6,448,607 B1	09/10/2002	Hsu	12/03/2001
TH	6,496,034 B2	12/17/2002	Forbes	02/09/2001

Examiner Signature	TH-TH 710	Date Considered	Sept. 2005
*Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.			

First Named Inventor	Leonard Forbes	SECOND SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT FORM PTO-1449
Serial No.	10/785,310	
Filing Date	February 24, 2004	
Group Art Unit	2818	
Examiner Name	Tu Tu V. Ho	
Allowed	April 28, 2003	
Confirmation No.	9673	
Attorney Docket No.	400.264US01	
Title: 4F2 EEPROM NROM MEMORY ARRAYS WITH VERTICAL DEVICES		Sheet 2 of 2

U.S. Patent References				
Examiner Initials	Document No.	Issue/Publication Date	Name	Filing Date
THP	6,577,533 B2	06/10/2003	Sakui	03/23/2001
	6,597,037 B1	07/22/2003	Forbes	09/26/2000
	6,639,268 B2	10/28/2003	Forbes	05/20/2002
	6,642,572 B2	11/04/2003	Kusumi	03/07/2003
	6,657,250 B1	12/02/2003	Rudeck	08/21/2002
	6,680,508 B1	01/20/2004	Rudeck	08/28/2002
	6,762,955 B2	07/13/2004	Sakui	04/25/2003
THP	6,768,162 B1	07/24/2004	Chang	08/05/2003

Foreign Patent References					
Examiner Initials	Foreign Patent		Name	Publication Date	Translation
	Country	No.			
THP	JP	01053577	Toshiba Corp.	01/03/0989	Y
THP	JP	05251711	OKI Electric Ind. Co. Ltd.	09/28/1993	Y
THP	EP	0 562 257 A1	International Business Machines Corp.	09/29/1993	
THP	EP	0 485 018 A2	N.V. Philips Electronics	11/08/1990	
THP	EP	1 271 652 A	Sharp	01/02/2003	

Other References	
Examiner Initials	Author, Title, Date, Pages, etc.
THP	P. Cappelletti, et al, "Failure Mechanisms of Flash Cell in Program/Erase Cycling", IEEE, 1994, IEDM, pp. 291-294
THP	H. Guan, et al, "On Scaling of SST Split-Gate Flash Memory Technologies", Department of Electrical and Computer Engineering, University of California, Irvine, Final Report 1998-1999 for MICRO Project 98-080.
THP	P. Pavan, et al, "Flash Memory Cells – an Overview" Proceedings of the IEEE, Vol. 85, No. 8, August 1997, pp. 1248-1271.
THP	S. Jin Ho, et al, "Charge-to-Breakdown Characteristics of Thin Gate Oxide and Buried Oxide on SIMOX SOI Wafers", Journal of the Electrochemical Society, Vol. 144, No. 1, January 1997, pp. 375-378.

Examiner Signature	TU THP THO	Date Considered	Sept - 2005
*Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.			